

Amendments to the Claims:

This listing of claims will replace all prior versions, and listings, of claims in the application:

Listing of Claims:

1. (Original) A driving apparatus for a plasma display panel in which one frame has a plurality of sub-fields, said apparatus comprising: sub-field mapping means for mapping a data inputted from the exterior thereof onto a sub-field pattern stored in advance; an APL calculator for calculating an APL corresponding to said data inputted from the exterior and generating an information about the number of sustaining pulses corresponding to the calculated APL; a load detector for receiving the mapped data from the sub-field mapping means to generate a control signal in response to whether or not a data for each sub-field is supplied; and a waveform generator for controlling a sustaining pulse applied to a panel in response to said information about the number of sustaining pulses and said control signal.

2. (Original) The driving apparatus as claimed in claim 1, wherein the load detector generates said control signal in correspondence with a sub-field to which said data is not supplied, of the plurality of sub-fields.

3. (Original) The driving apparatus as claimed in claim 2, wherein the waveform generator makes a control such that said sustaining pulse is not applied during a sustaining period of a sub-field corresponding to said control signal while said sustaining pulse is applied during sustaining periods of the remaining sub-fields.

4. (Canceled).

5. (Canceled).

6. (New) A plasma display panel comprising:
a first substrate;
a plurality of first electrodes provided on the first substrate;
a plurality of second electrodes provided on the first substrate, the first and second electrodes being provided in a first direction;
a second substrate;
a plurality of address electrodes provided on the second substrate in a second direction, the first direction being different from the second direction;
a plurality of barrier ribs provided on the second substrate in the second direction;
a plurality of discharge cells, each cell provided between two adjacent barrier ribs, and having corresponding first, second and address electrodes;

a first circuit for driving the address electrodes;

a second circuit for driving at least one of the first electrodes or the second electrodes, wherein

during at least one sub-field of a frame, at least one sub-field having an address period and a sustain period, the second circuit omit sustain signals to at least one of the first electrodes or the second electrodes during the sustain period of the at least one sub-field to provide a constant voltage.

7. (New) The plasma display panel of claim 6, wherein the constant voltage is a ground potential.

8. (New) The plasma display panel of claim 6, wherein the at least one sub-field of the frame further comprises a reset period.

9. (New) The plasma display panel of claim 6, wherein a gray level is full black.

10. (New) A method of driving a plasma display panel, comprising:
driving a plasma display panel based on a plurality of sub-fields within a frame to provide a gray level in a plasma display panel, each sub-field having an address period and a sustain period, the plasma display panel having

a first substrate,
a plurality of first electrodes provided on the first substrate,
a plurality of second electrodes provided on the first substrate, the first and second electrodes being provided in a first direction,
a second substrate,
a plurality of address electrodes provided on the second substrate in a second direction, the first direction being different from the second direction,
a plurality of barrier ribs provided on the second substrate in the second direction,
a plurality of discharge cells, each cell provided between two adjacent barrier ribs,
and having corresponding first, second and address electrodes,
driving the address electrodes using a first circuit during the address period of at least one sub-field, and
driving at least one of the first electrodes or the second electrodes using a second circuit during the sustain period of the at least one sub-field, wherein the second circuit provides a constant voltage to at least one of the first electrodes or the second electrodes during entire period of the sustain period of the at least one sub-field.

11. (New) The method of claim 10, wherein the constant voltage is a ground potential.

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12. (New) The method of claim 10, wherein the at least one sub-field of the frame further comprises a reset period.

13. (New) The method of claim 10, wherein a gray level is full black.